Results 1 - 10 of about 181,000 for WIRELESS COMMUNICATION RECEIVER HAVING AN ADC WITH A LIM

WIRELESS COMMUNICATION RECEIVER HAVING AN ADC WITH A LIMITED ...
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Wireless communication standards usually define receiver ..... Hence, the residual dynamic range due to the limited filter ...
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Source, EURASIP Journal on Wireless Communications and Networking archive ... A 67 dB dynamic range was measured with a sampling frequency of 37.5 MHz. ...
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1 WIRELESS COMMUNICATION RECEIVER HAVING AN ADC WITH A LIMITED DYNAMIC RANGE

Inventor: QIAN XUECHENG (CN)

Applicant: KONINKL PHILIPS ELECTRONICS NV (NL)

EC: H04B1/10S

IPC: *H04B1/10*; H04B1/10; (IPC1-7): H04B1/10

Publication info: EP1627471 - 2006-02-22

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	Title	Pub. Date	Int. Class	App. Num	Applicant
1.	(WO 2004/102819) WIRELESS COMMUNICATION RECEIVER HAVING AN ADC WITH A LIMITED DYNAMIC RANGE	25.11.2004	H04B 1/10	PCT/IB2004/050467	KONINKLIJKE ELECTRONIC

The present invention provides a low cost receiver by reducing the required dynamic range of the ADC in a wireless comm receiver, without degrading the receiver performance. In the wireless communication receiver of the invention, a digital filte filter digital signals from the ADC to attenuate residual interferers in the digital signals by a predetermined amount (e.g., m prescribed in a technical specification). This allows relaxation of tolerable quantization noise generated by the ADC to a pr level to thereby substantially reduce a dynamic range of the ADC. This pre-defined level of quantization noise is higher tha prescribed by the receiver's sensitivity, while the total interference o...

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Search Time: 0.06 seconds.

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(WO/2004/102819) WIRELESS COMMUNICATION RECEIVER HAVING AN ADC WITH A LIMITED DYNAMIC RANGE

Biblio. Data | Description | Claims | National Phase | Notices | Documents

Latest bibliographic data on file with the International Bureau

Pub. No.: WO/2004/102819 International Application No.: PCT/IB2004/050467
Publication Date: 25.11.2004 International Filing Date: 16.04.2004

IPC: *H04B 1/10* (2006.01)

Applicants: KONINKLIJKE PHILIPS ELECTRONICS N.V. [NL/NL]; Groenewoudseweg 1, NL-5621 BA Eindhoven (NL) *(All Except US)*.
QIAN, Xuecheng [CN/CN]; Philips Electronics China, 21/F Kerry Office Building 218 Tian Mu, Xi Road, Shanghai 200070 (CN) *(US Only)*.

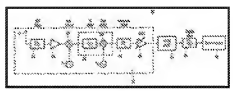
Inventor: QIAN, Xuecheng; Philips Electronics China, 21/F Kerry Office Building 218 Tian Mu, Xi Road, Shanghai 200070 (CN).

Agent: KONINKLIJKE PHILIPS ELECTRONICS N.V.; c/o Van der Veer, Johannis, L., Prof. Holstlaan. 6, NL-5656 AA Eindhoven (NL).

Priority Data: 03131265.5 16.05.2003 CN

Title: WIRELESS COMMUNICATION RECEIVER HAVING AN ADC WITH A LIMITED DYNAMIC RANGE

Abstract: The present invention provides a low cost receiver by reducing the required dynamic range of the ADC in a wireless communication receiver, without degrading the receiver performance. In the wireless communication receiver of the invention, a digital filter is used to filter digital signals from the ADC to attenuate residual interferers in the digital signals by a predetermined amount (e.g., more than that prescribed in a technical specification). This allows relaxation of tolerable quantization noise generated by the ADC to a pre-defined level to thereby substantially reduce a dynamic range of the ADC. This pre-defined level of quantization noise is higher than a level prescribed by the receiver's sensitivity, while the total interference of the receiver is kept at a level not greater than an allowable level. Thus, the ADC has a word length that corresponds to the reduced dynamic range. Accordingly, not only the cost of the ADC is decreased, the costs of all signals processing modules following the ADC are also decreased, resulting in a substantial reduction in the overall cost of the receiver.



Designated States: AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NA, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.
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Publication Language: English (EN)

Filing Language:	English (EN)

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Results 1 - 10 of about 4,320 for "digital filter" ADC "dynamic range" "quantization noise". (0.34 seconds)

A 2.7V 11.8 mW baseband ADC with 72 dB dynamic range for GSM ...
frequency of the digital filter is around 90 kHz) and. with a 32 kHz sinewave input. A peak SNDR of 66. dB and a dynamic range of 72. dB. are achieved. ...
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... that of the original ADC, due to. the action of the digital filter on quantization noise. .... problem arises due to the finite dynamic range of the ADC ...
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The word length depends on the dynamic range requirement of the ADC. The lower limit of the dynamic range is specified by the equivalent quantization noise ...
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[PPT] System Aspects of ADC Design
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Quantization noise is pushed out of the signal band; Digital filter required to .... Trade off between filter order and dynamic range lost in the ADC ...
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The digital filter removes the high frequency quantization noise and down samples the ...
Keywords: sigma-delta ADC; modulator; decimation; dynamic range ...
www.paper.edu.cn/download_feature_paper.php?serial_number=Vengy2007-016 -
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Analog Devices: MT-023: ADC Architectures IV: Sigma-Delta ADC ...
The frequency response of the digital filter in the AD1871 ADC is shown in ... from the first DAC output to yield the first stage quantization noise, Q1. ...
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ADC resolution set by. 1. Dynamic Range. 2. Quantization Noise ... Spurs cannot be filtered with anti-alias filter or digital filter. ...
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5 illustrates a transfer function of a digital filter In accordance with one ... Thus, the permissible quantization noise of the ADC is greatly relaxed to ...
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This allows relaxation of tolerable quantization noise generated by the ... Thus , the ADC has a word length that corresponds to the reduced dynamic range. ...
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Then, the digital filter filters the digital signals from the ADC and ... Thus, the ADC has a word length that corresponds to the reduced dynamic range. ...
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The ADC is operable to directly sample an RF signal independent of a band pass filter. ... and reduced dynamic range due to aliasing and quantization noise. ...
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reduced dynamic range due to quantization noise, and folded signals due to folded signals ... this requirement; but it took years for digital filter ...
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The amount of circuitry of the digital filter is much larger than the analog ..... "Gain Scaling of Oversampled ADC", Order Inter- [6] Nay Sooch, 4851841. ...
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Quantization Noise. ADC. Fig. 2.7: Digital Channel Select Filtering ..... This allows a reduced dynamic range, requiring fewer bits to be ...
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semi-digital filter implemented as an RF-DAC. In contrast to that, ... A 4b 1.25GS/s for 2.5mW ADC will be presented in (Paper 31.1) ISSCC 2006. A new ...
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51. A new filter concept yielding improved resolution and throughput in rad
Abbiati, R.; Geraci, A.; Ripamonti, G.;
Nuclear Science Symposium Conference Record, 2004 IEEE
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Digital Object Identifier 10.1109/NSSMIC.2004.1462197

52. Analog shaping optimization for digital processing of radiation detector
Abbiati, R.; Geraci, A.; Ripamonti, G.;
Nuclear Science Symposium Conference Record, 2004 IEEE
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53. GARFIELD: a General ARray for Fragment Identification and for Emitted Dissipative collisions
Gramegna, F.; Moroni, A.; Casini, G.; Bruno, M.; Abbondanno, U.; Bassinr, R
Boiano, C.; Brambilla, S.; Cavaletti, R.; Chiari, M.; Cortesi, A.; D'Agostino, M.
M.; Geraci, E.; Giacchini, M.; Giussani, A.; Kravchuk, V.L.; Lanchais, A.L.; Ma
G.V.; Mastinu, P.F.; Milazzo, P.M.; Nannini, A.; Ordine, A.; Tobia, G.; Tonetto
G.; Vannucci, L.;
Nuclear Science Symposium Conference Record, 2004 IEEE
Volume 2, 16-22 Oct. 2004 Page(s):1132 - 1136 Vol. 2
Digital Object Identifier 10.1109/NSSMIC.2004.1462402

54. Self-configuring digital processor for on-line pulse analysis
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Nuclear Science Symposium Conference Record, 2003 IEEE
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55. **Digital pulse-shape acquisition from CHIMERA telescopes**
Alderighi, M.; Anzalone, A.; Auditore, L.; Arena, N.; Bassini, R.; Boiano, C.; Bi
Cavallaro, S.; D'Andrea, M.; De Filippo, E.; Geraci, E.; Ghilardi, D.; Giustolisi,
Guazzoni, P.; Laguidara, E.; Lanzano, G.; Lanzalone, G.; Nicotra, D.; Opichal
Pagano, A.; Papa, M.; Pirrone, S.; Politi, G.; Porto, F.; Rosato, E.; Russo, S.;
Sechi, G.; Trifiro, A.; Trimarchi, M.; Urso, S.; Vigilante, M.; Zetta, L.;
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56. **A low-complexity high-performance digital control architecture for volta**
Saggini, S.; Ghioni, M.; Geraci, A.;
Power Electronics Specialist Conference, 2003. PESC '03. 2003 IEEE 34th A
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57. **A new method for LMS synthesis of optimum finite impulse response (F time and frequency constraints and noises**
Riboldi, S.; Geraci, A.; Abbiati, R.; Gatti, E.; Ripamonti, G.;
Nuclear Science Symposium Conference Record, 2002 IEEE
Volume 1, 10-16 Nov. 2002 Page(s):198 - 202 vol.1
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58. **A new class of optimum filters with complete rejection of periodic noise**
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59. **Processing CsI(Tl) 2D matrices by means of neural networks and Marko**
Alderighi, M.; Anzalone, A.; Baruzzi, R.; Cardella, G.; Cavallaro, S.; De Filippc
F.; Guazzoni, P.; Lanzalone, G.; Lanzano, G.; LoNigro, S.; Pagano, A.; Papa,
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60. **Dynamically reconfigurable architectures for on-line digital pulse analys**
Di Odoardo, A.; Riboldi, S.; Geraci, A.; Ripamonti, G.;
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61. **An algorithm for 3D localization of multiple pulses in large-volume segn**
Gatti, E.; Casati, G.; Geraci, A.; Riboldi, S.; Ripamonti, G.; Camera, F.; Millior
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Volume 2, 15-20 Oct. 2000 Page(s):9/24 - 9/28 vol.2
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62. **A novel approach to the classification of multidetector array data**
Alderighi, M.; Anzalone, A.; Bartolucci, M.; Bruno, M.; Cardella, G.; Cavallaro, Filippo, E.; Geraci, E.; Giustolisi, F.; Guazzoni, P.; Lanzalone, G.; Lanzano, G Pagano, A.; Papa, M.; Pirrone, S.; Politi, G.; Porto, F.; Russo, S.; Sambataro, Zetta, L.;
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Volume 1, 15-20 Oct. 2000 Page(s):6/286 - 6/289 vol.1
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63. **Efficiency of segmented HPGe γ-ray detectors: design criteria for pulse**
Wieland, O.; Camera, F.; Million, B.; Bracco, A.; Pignanelli, M.; Ripamonti, G. J.;
Nuclear Science Symposium Conference Record, 2000 IEEE
Volume 1, 15-20 Oct. 2000 Page(s):8/1 - 8/5 vol.1
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64. **Evolution of the DISN-Pacific towards an integrated ATM infrastructure**
El-Mokadem, F.; Geraci, J.A.;
Military Communications Conference Proceedings, 1999. MILCOM 1999. IEE
Volume 1, 31 Oct.-3 Nov. 1999 Page(s):230 - 234 vol.1
Digital Object Identifier 10.1109/MILCOM.1999.822678
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65. **Measurement requirements and front-end design rules for gamma-ray tr germanium detectors through pulse-shape analysis**
Ripamonti, G.; Pullia, A.; Geraci, A.;
Instrumentation and Measurement Technology Conference, 1999. IMTC/99. I IEEE
Volume 3, 24-26 May 1999 Page(s):1916 - 1920 vol.3
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66. **Design and analysis of nondestructive multiple readout in high resolutic**
Castoldi, A.; Gatti, E.; Geraci, A.; Guazzoni, C.; Longoni, A.;
Nuclear Science Symposium, 1999. Conference Record. 1999 IEEE
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67. **A pulse-shape analysis approach to 3-D position determination in large- detectors**
Gatti, E.; Casati, G.; Geraci, A.; Pullia, A.; Ripamonti, G.;
Nuclear Science Symposium, 1999. Conference Record. 1999 IEEE
Volume 1, 24-30 Oct. 1999 Page(s):346 - 351 vol.1
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68. **Automatic pole-zero/zero-pole digital compensator for high-resolution s experiments**
Geraci, A.; Pullia, A.; Ripamonti, G.;
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Volume 2, 8-14 Nov. 1998 Page(s):891 - 895 vol.2

Digital Object Identifier 10.1109/NSSMIC.1998.774313

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69. **Extending a monoprocessor real-time system in a DSP-based multiproc**
Aiello, S.; Anzalone, A.; Bartolucci, M.; Cardella, G.; Cavallaro, S.; De Filippo
Geraci, M.; Giusolisi, F.; Guazzoni, P.; Iacono Manno, M.; Lanzalone, G.; Lan
Manfredi, G.; Pagano, A.; Papa, M.; Pirrone, S.; Politi, G.; Porto, F.; Rizzo, F.
G.; Sperduto, L.; Sutera, C.; Zetta, L.;
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Volume 1, 25-27 Aug. 1998 Page(s):208 - 211 vol.1
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70. **Digital vs. analogue spectroscopy: a comparative analysis**
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Volume 1, 18-21 May 1998 Page(s):666 - 669 vol.1
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71. **A new numerical method for determining the excess noise power spectr**
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72. **A 20 MB/s data rate 2.5 V flash memory with current-controlled field era endurance**
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73. **On-field determination of the minimum-noise filter for digital radiation s**
Pullia, A.; Geraci, A.; Ripamonti, G.;
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74. **Shaping the quantization noise in high resolution digital spectroscopy:**
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75. **Self-calibration and self-optimization in DSP-based high resolution spec**
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1. A 16X oversampling CMOS ADC with 100 kHz bandwidth and 90dB SNR
Dedic, I.J.;
Advanced A-D and D-A Conversion Techniques and their Applications, 1994. Conference on
6-8 Jul 1994 Page(s):82 - 89
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1. WIRELESS COMMUNICATION RECEIVER HAVING AN ADC WITH A L
QIAN, Xuecheng (KONINKLIJKE PHILIPS ELECTRONICS N.V.
TREATY APPLICATION, Nov 2004
patno:WO04102819
...Then, the digital filter filters the...from the ADC and attenuates noise generated...corresponds to the reduced dynamic range. A (ADC) that converts...and (c) a digital filter that filters...from the attenuates...tolerable quantization noise generated...correspond dynamic range. 2. The receiver...
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2. Wireless communication receiver having an adc with a limited dyna
Qian, Xuecheng, *UNITED STATES PATENT AND TRADEMARK OFFI PUBLICATION*, Nov 2006
patno:US20060251186
...Then, the digital filter filters the...from the ADC and attenuates noise generated...corresponds to the reduced dynamic range. [Accordingly...converter (ADC) that converts...and (c) a digital filt ADC and attenuates...tolerable quantization noise generated...c reduced dynamic range. 2. The receiver...
Full text available at patent office. For more in-depth search
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3. OVERSAMPLING AND DIGITALLY FILTERING RFID SIGNALS
Frederick, Thomas J. / Schaffer, James M. / Repke, Joseph F Inc.), *UNITED STATES PATENT AND TRADEMARK OFFICE PRE-GRA*

patno:US20070291884
...distortion and reduced dynamic range due to aliasing and qua reader...aliasing and quantization noise. When using...or comple implementations...bit. The ADC 112 can...signal-to-quantization
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4. i Abstract Recent efforts in the design of wireless RF transceivers...
Dec 1997
...anti-alias filter and the ADC, and is then filtered...pass filter to p aliasing when it is...Channel From Mixer To DSP ADC I Q 200KS/s Channel Quantization Noise GSM DECT Bypass Low...
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5. Adaptive digital predistortion linearizer for power amplifiers in milit
Patel, Jayanti. , Jan 2004
Thesis (M.S.E.E.)--University of South Florida, 2004. Title from PDI formatted into pages; contains 98 pages. Includes bibliographical r
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6. Adaptive digital predistortion linearizer for power amplifiers in milit
Patel, Jayanti. , Jan 2004
Thesis (M.S.E.E.)--University of South Florida, 2004. Title from PDI formatted into pages; contains 98 pages. Includes bibliographical r
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7. Low distortion band-pass analog to digital converter with feed forw
Cooper, Scott A. / Esterberg, Aanand L. (Impinj, Inc.), *UNITI TRADEMARK OFFICE GRANTED PATENT*, Oct 2005
patno:US6954159
...section is provided in this ADC as in FIG. 1a but not shown...low modulator differs from the FIG. 1a ADC modulator by including the reduced dynamic range and matching accuracies required...
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8. swp0003.dvi [PDF-5MB]
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...85 4.12 System Noise Figure vs. Amplifier-Noise-to-Quantizatio Overview...78 4.5 Receiver Requirements, ADC . . . . . [http://www.nt.tuwien.ac.at/mobile/theses_finished/PhD_...]
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9. PowerPoint Presentation [PPT-2MB]
Nov 2005
...bits, but speed ~ 2 MHz Digital filter removes components at o Quantization noise must not limit sensing Strong...feedback arou Prediction Loop...domain prior to A/D for reduced dynamic range [http://www.eecs.berkeley.edu/~sahai/Presentations/DySP...]
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10. PowerPoint Presentation [PPT-2MB]
Jan 2007
...bits, but speed ~ 2 MHz Digital filter removes components at o Quantization noise must not limit sensing Strong...feedback arou Prediction Loop...domain prior to A/D for reduced dynamic range [http://web.cecs.pdx.edu/~mperkows/CAPSTONES/MONDAY_SEN.
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- low-pass
- detectors
- periodic signals
- adaptive filter
- autocorrelation
- feature detectors

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11. DIGITAL CIRCUIT FOR A FREQUENCY MODULATION AND CARRIER RADIO SYSTEM
REED, Roger (REED, Roger), *PATENT COOPERATION TREATY AP*
patno: WO9107828
...to analog converter (DAC), thereby reducing the synthesizer qua equivalent bil: precision in the digital to analog conversion...microp beyond the analog interface circuits 40, ADC and DAC circuits 30 c and digital...
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"digital filter" ADC "reduced dynamic range Search

Results of searching in PCT for:
wo/1991/07828: 1 record
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Refine Search wo/1991/07828

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	Title	Pub. Date	Int. Class	App. Num	A
1.	(WO 1991/007828) DIGITAL CIRCUIT FOR A FREQUENCY MODULATION AND CARRIER SYNTHESIS IN A DIGITAL RADIO SYSTEM	30.05.1991	H03C 3/09	PCT/US1990/005988	F

An integrated, multimode FM radio system including a common reference clock (4) providing synchronized digital signal pr both transmission (1) and reception (2). The radio system according to the present invention also includes a novel digital fr synthesizer (3, 8, 9, 43, 44), a digital FM demodulator (17) and a digital FM modulator (16) which together provide improve modulation and demodulation fidelity thereby assuring interoperation with other radios in all analog and digital modulation improved spectral purity and faster channel switching speed for the frequency hopping synthesizer (71), improved reliabilit reduction in complexity, and reduction of radio production cost, including reduction in te...

Search Summary

WO/1991/07828: 1 occurrence in 1 record.

Search Time: 0.06 seconds.

Correspondence Address for 10/556249

Customer Number	Contact Information	Address
65913 Delivery Mode: Electronic	Telephone: (408)474-9077 Fax: No Fax # E-Mail: ip.department.us@nxp.com	NXP, B.V. NXP INTELLECTUAL PROPERTY DEPARTMENT M/S41-SJ 1109 MCKAY DRIVE SAN JOSE CA 95131

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Parent Data
10556249, filed 11/10/2005
is a national stage entry of PCT/IB04/50467 International Filing Date: 04/16/2004

Child Data
No Child Data

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03131365.5	05/16/2003	CHINA

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Application Number Information

Application Number: 10/556249
Assignments

Examiner Number: 80488 / TORRES, JUAN

Filing or 371(c) Date: 11/10/2005 eDan
Group Art Unit: 2611 IFW Madras

Effective Date: 11/10/2005
Class/Subclass: 375/350.000

Application Received: 11/10/2005
Lost Case: NO

Pat. Num./Pub. Num: /20060251186
Interference Number:

Issue Date: 00/00/0000
Unmatched Petition: NO

Date of Abandonment: 00/00/0000
L&R Code; Secrecy Code:1

Attorney Docket Number: CN 030011
Third Level Review: NO
Secrecy Order: NO

Status: 30 /DOCKETED NEW CASE - READY FOR EXAMINATION
Status Date: 10/14/2006

Confirmation Number: 6573
Oral Hearing: NO

Title of Invention: WIRELESS COMMUNICATION RECEIVER HAVING AN ADC WITH A LIMITED DYNAMIC RANGE

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IDS Information

Content	Mailroom Date	Entry Number	IDS Review	Last Modified	Reviewer
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Inventor Information for 10/556249

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Inventor Name Search Result

Your Search was:

Last Name = QIAN
First Name = XUECHENG

Application#	Patent#	Status	Date Filed	Title	Inventor Name
10556249	Not Issued	30	11/10/2005	Wireless communication receiver having an adc with a limited dynamic range	QIAN, XUECHENG
10557381	Not Issued	161	11/18/2005	Multi-band and multi-mode mobile terminal for wireless communication systems	QIAN, XUECHENG
10572846	Not Issued	30	03/21/2006	Methods and system for controlling an illuminating apparatus	QIAN, XUECHENG
10581805	Not Issued	30	06/02/2006	Receiver For Wireless Communications	QIAN, XUECHENG
10581808	Not Issued	30	06/02/2006	Receiver architecture for wireless communication	QIAN, XUECHENG
10588255	Not Issued	30	08/02/2006	Bandpass sampling receiver and the sampling method	QIAN, XUECHENG
11574738	Not Issued	160	01/01/0001	WIRELESS COMMUNICATION APPARATUS WITH MULTI-ANTENNA AND METHOD THEREOF	QIAN, XUECHENG
11813993	Not Issued	30	07/13/2007	Low Intermediate Frequency Receiver and the Same Method Thereof	QIAN, XUECHENG
12162175	Not Issued	19	01/01/0001	METHOD AND APPARATUS FOR SAMPLING RF SIGNALS	QIAN, XUECHENG

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